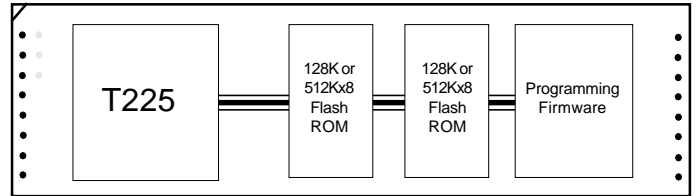
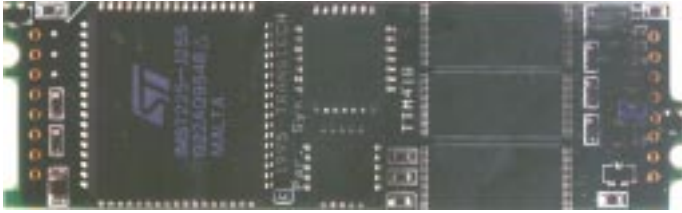


# TTM418

# FLASH ROM TRAM



The TTM418 Flash ROM TRAM is designed primarily for configuring and bootstrapping transputer networks in embedded systems.

After reset, the TTM418 feeds a program stored in the ROM through one of its four OS-Links. On-board software allows the Flash ROM to be reprogrammed without removing the ROM devices from the board or the TTM418 from an assembled system. Programming is via a simple protocol on one of the OS-Links. Safeguards are provided against accidental erasure or reprogramming. The Flash ROM devices can be reprogrammed in excess of 10,000 times.

In a system incorporating a TTM418, reset signals from the TRAM (subsystem services) are used to reset the target system. The TTM418 incorporates power-on reset circuitry and so can provide power-on reset and automatic start-up for the target system. The TTM418 can have one or two link connections to the target system. One link carries program code to the target, the other can be used to configure C004 Link Switches prior to bootstrapping the target.

During software development the TTM418 can be installed into the system as it would be in the finished product. The development host is connected to the TTM418 using the *transparent mode*.

Resident software in a separate memory on the TTM418 allows for device programming, system booting and the various debug and transparency modes. The firmware is used by sending commands to the TTM418 in *bootstrap mode* on any link. When a command is received on a link, an appropriate action is made, with a response on the same link.

❖ **Direct Replacement for SGS Thomson IMSB418**

❖ **Non-Volatile RAM (256Kbyte or 1Mbyte)**

❖ **In-System Reprogrammable**

❖ **On-Board Programming Firmware**

❖ **16-bit IMST225 transputer**

❖ **Standard size 1 TRAM format**

## ORDERING INFORMATION

TTM418-256	256kbyte option
TTM418-1024	1Mbyte option